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136.1001.04

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Jack REGULA

Serial No.: 09/421,771

Filed:

October 19, 1999

For: Method and Apparatus for Fault Tolerant, Software Transparent and High Data Integrity Extension to a Backplane or

Bus Interconnect

Art Unit: 2664

Examiner:

Ajit G. PATEL

Tel:

(703) 308-5347

Office Action Mailed:

April 19, 2004

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## RESPONSE TO OFFICE ACTION

Sir:

This paper is responsive to the outstanding final Office action mailed on April 19, 2004 (the "Final Office Action") and to Advisory Action mailed on June 30, 2004. The Final Office Action set a three-month period for response. Because this Response is being filed timely within the